

FIG. 1

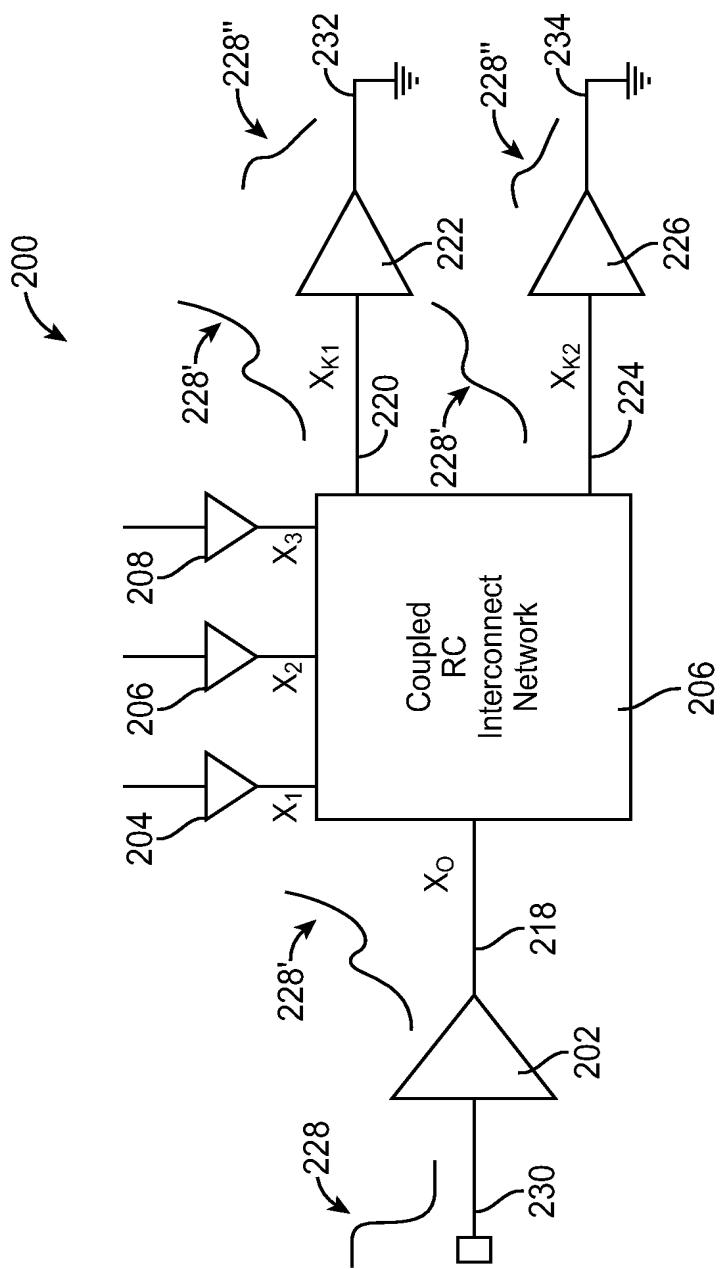


FIG. 2

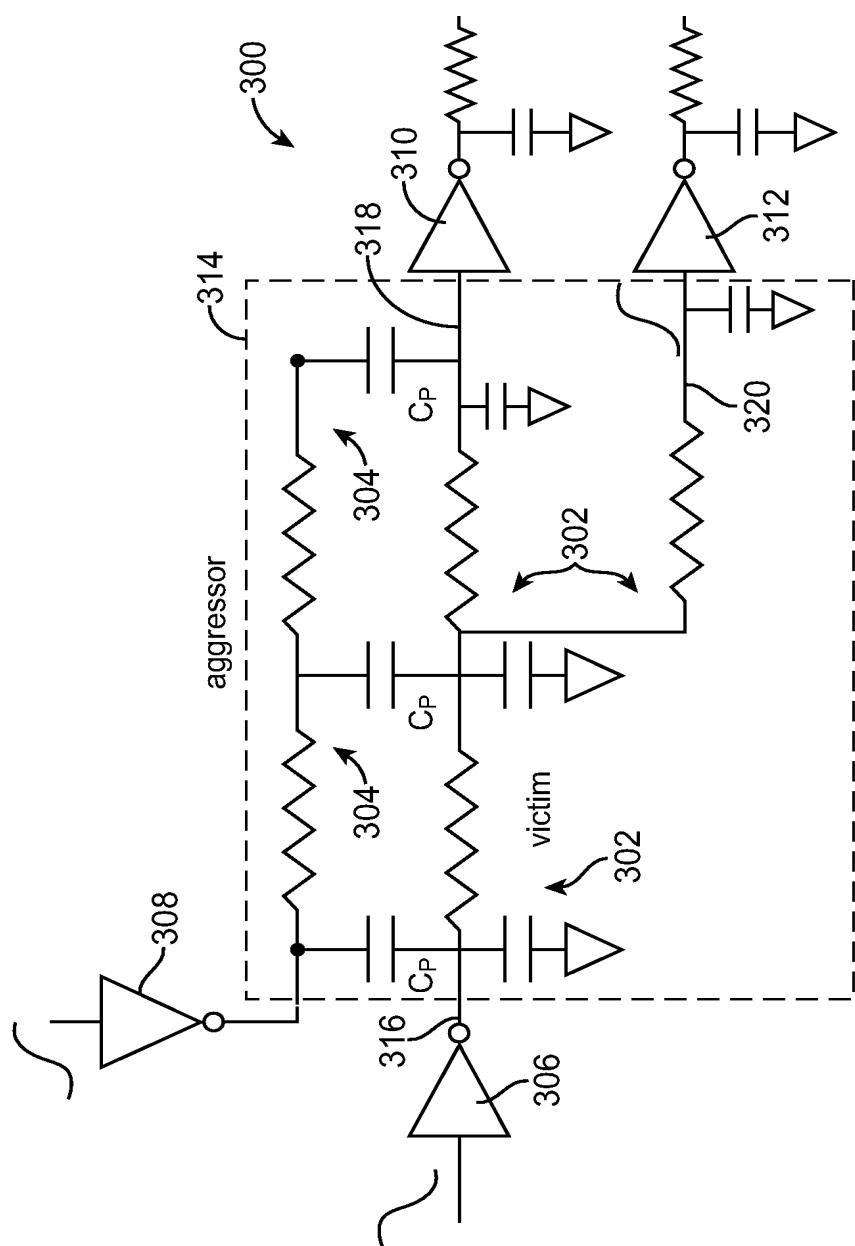


FIG. 3

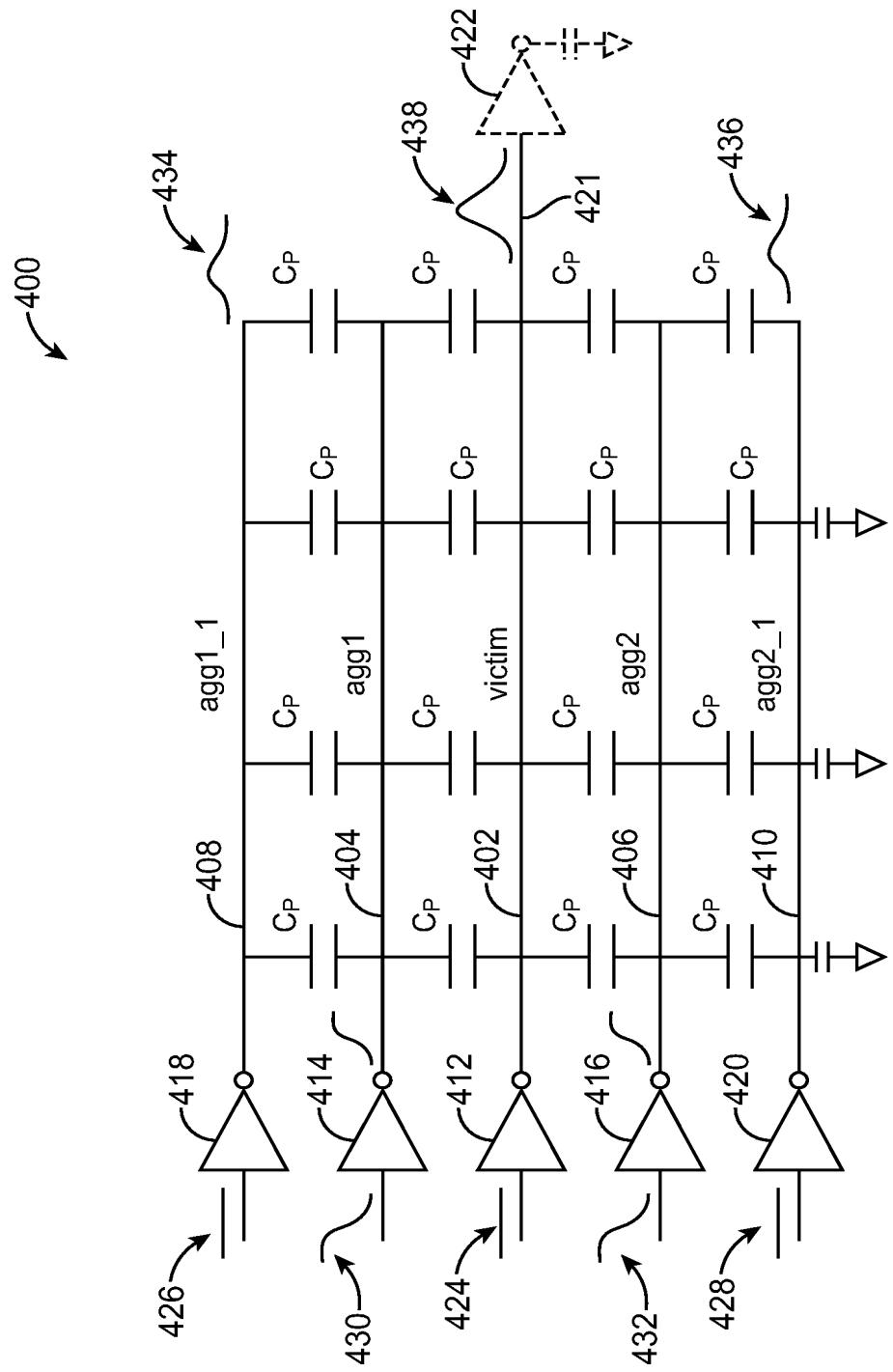


FIG. 4

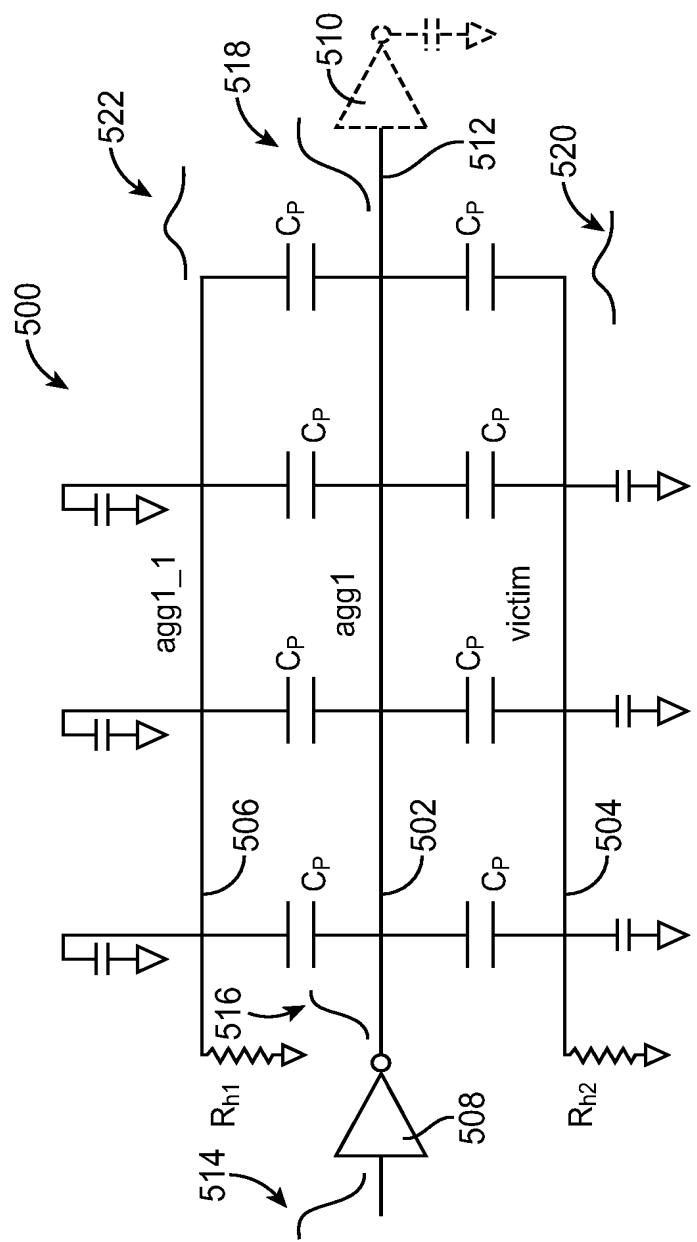
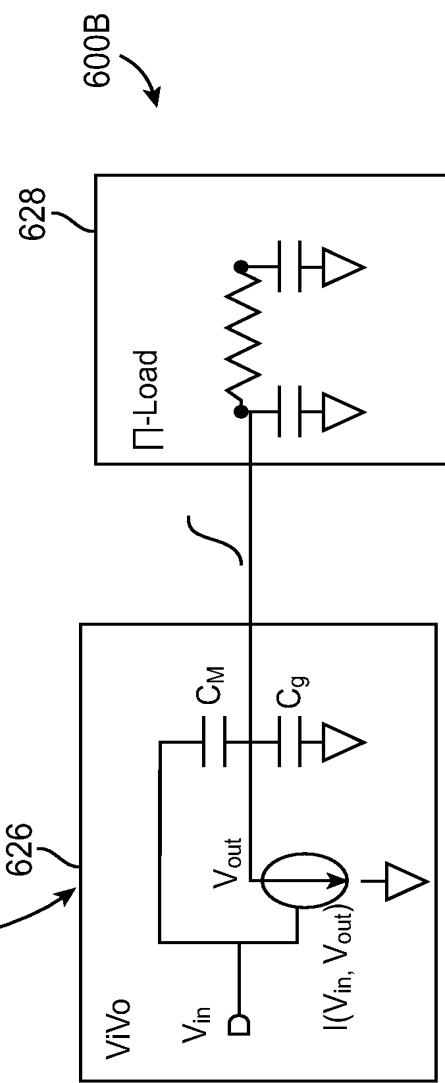
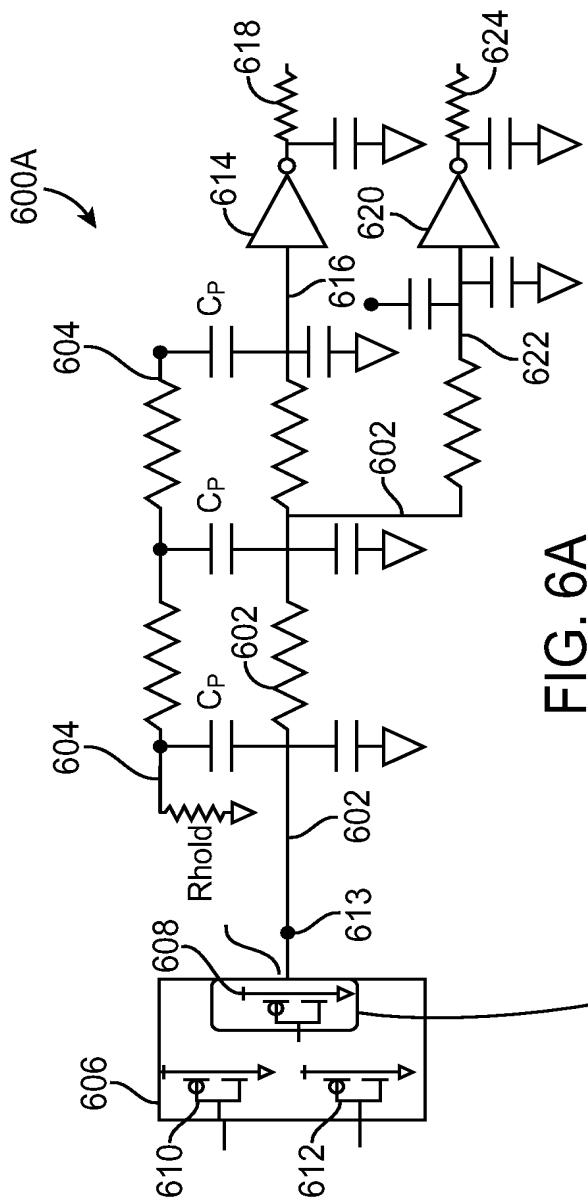


FIG. 5



V_{in}/V_{out}	V_{o0}	V_{o1}	V_{o2}	...	V_{oN}
V_{i0}	I_{00}	I_{01}	I_{02}	...	I_{0N}
V_{i1}	I_{10}	I_{11}	I_{12}	---	I_{1N}
V_{i2}	I_{20}	I_{21}	I_{22}	---	I_{2N}
.
.
.
V_{iM}	I_{M0}	I_{M1}	I_{M2}	...	I_{MN}

FIG. 7

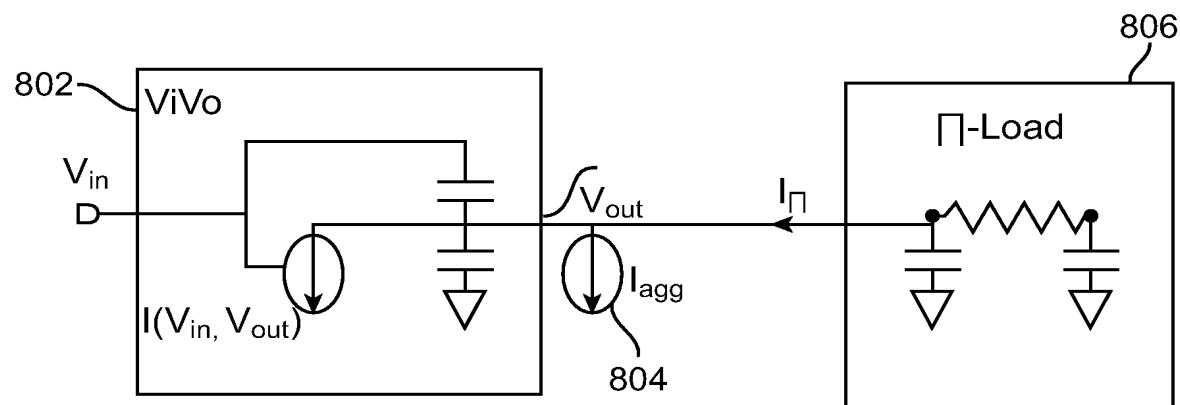


FIG. 8

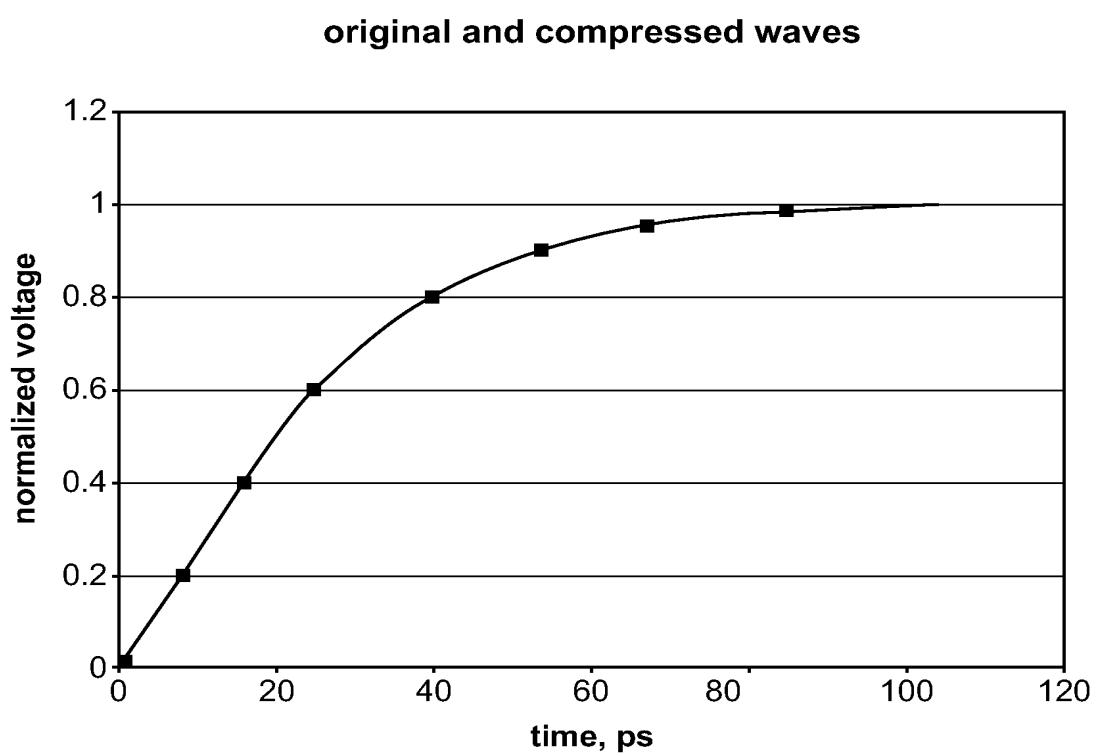


FIG. 9

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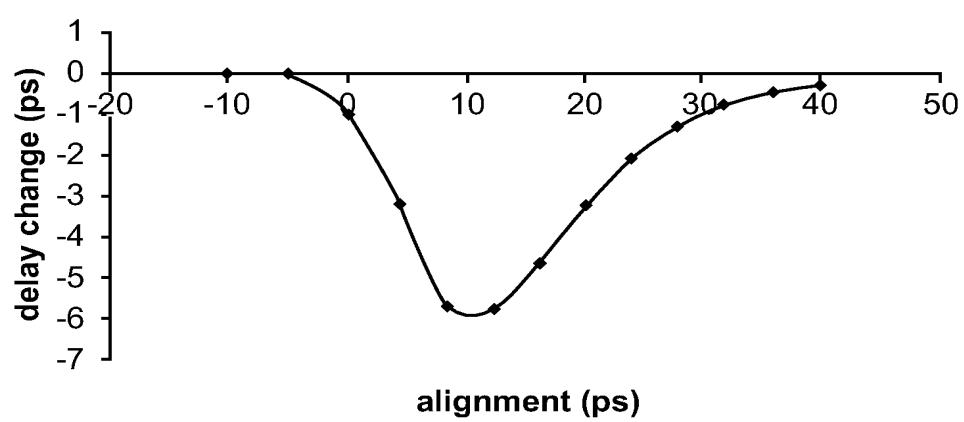


FIG. 10

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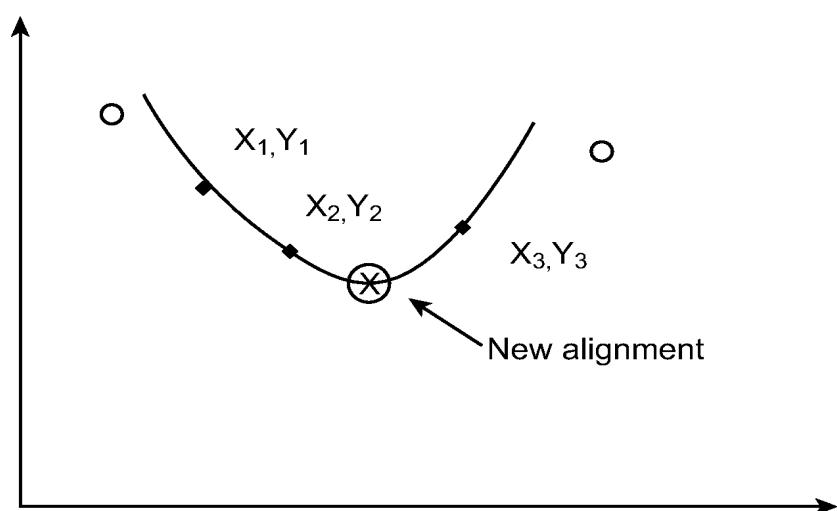


FIG. 11

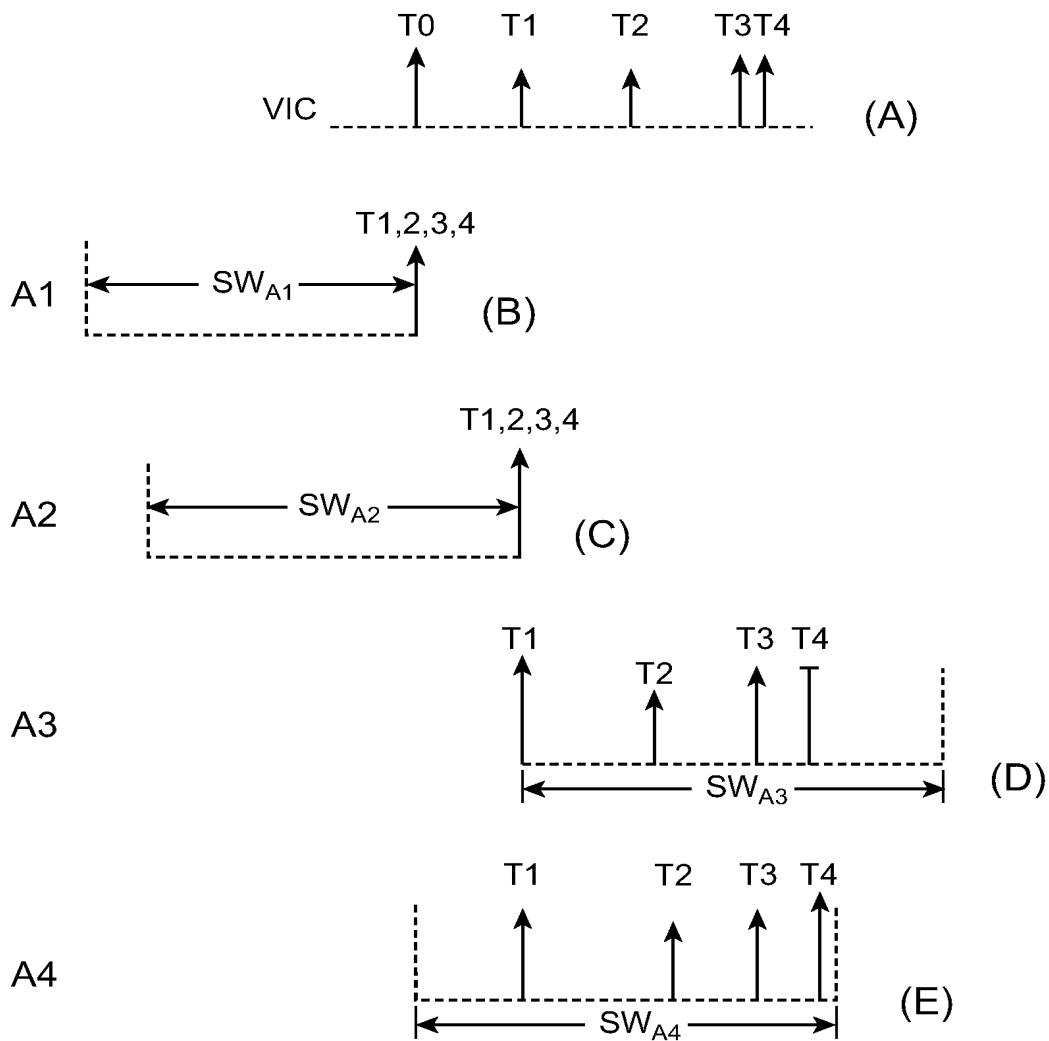


FIG. 12

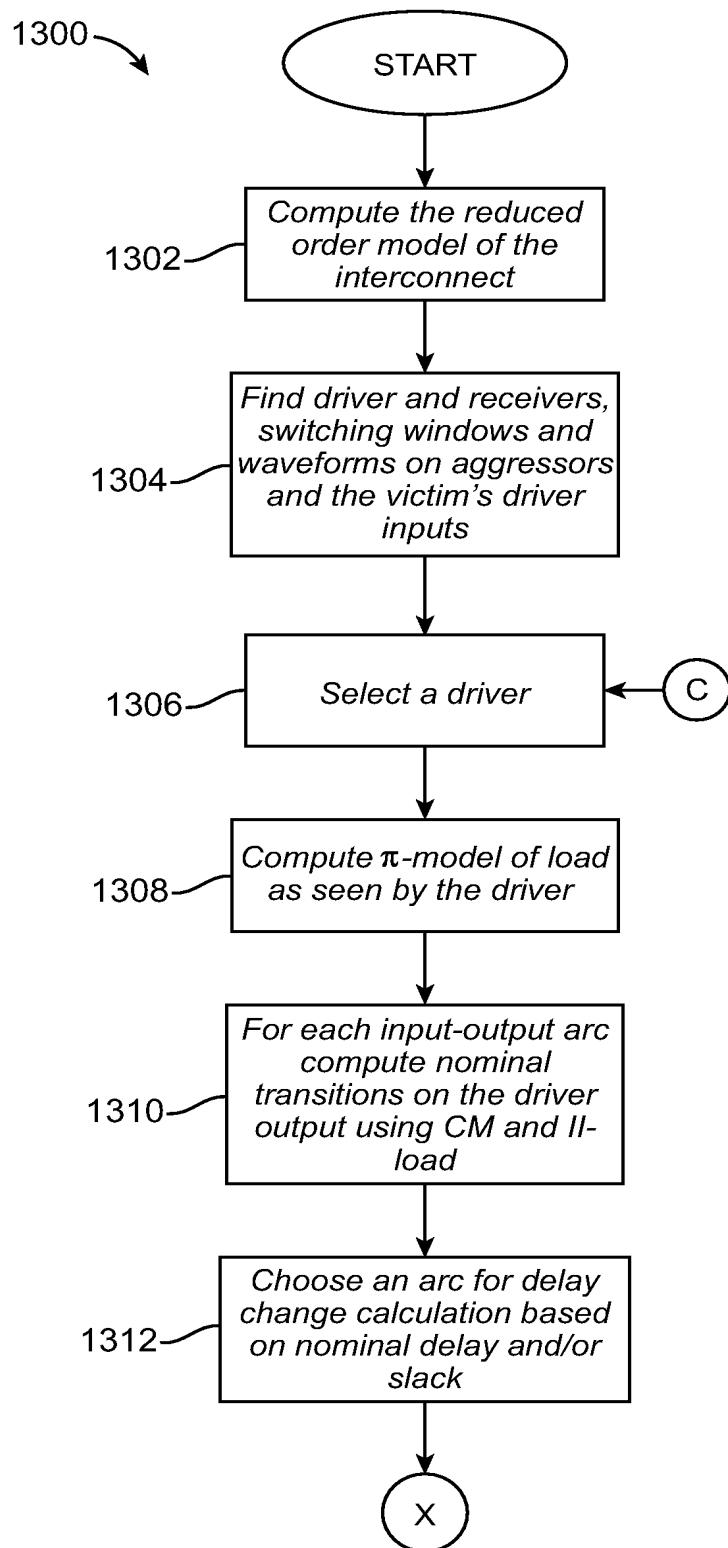


FIG. 13A

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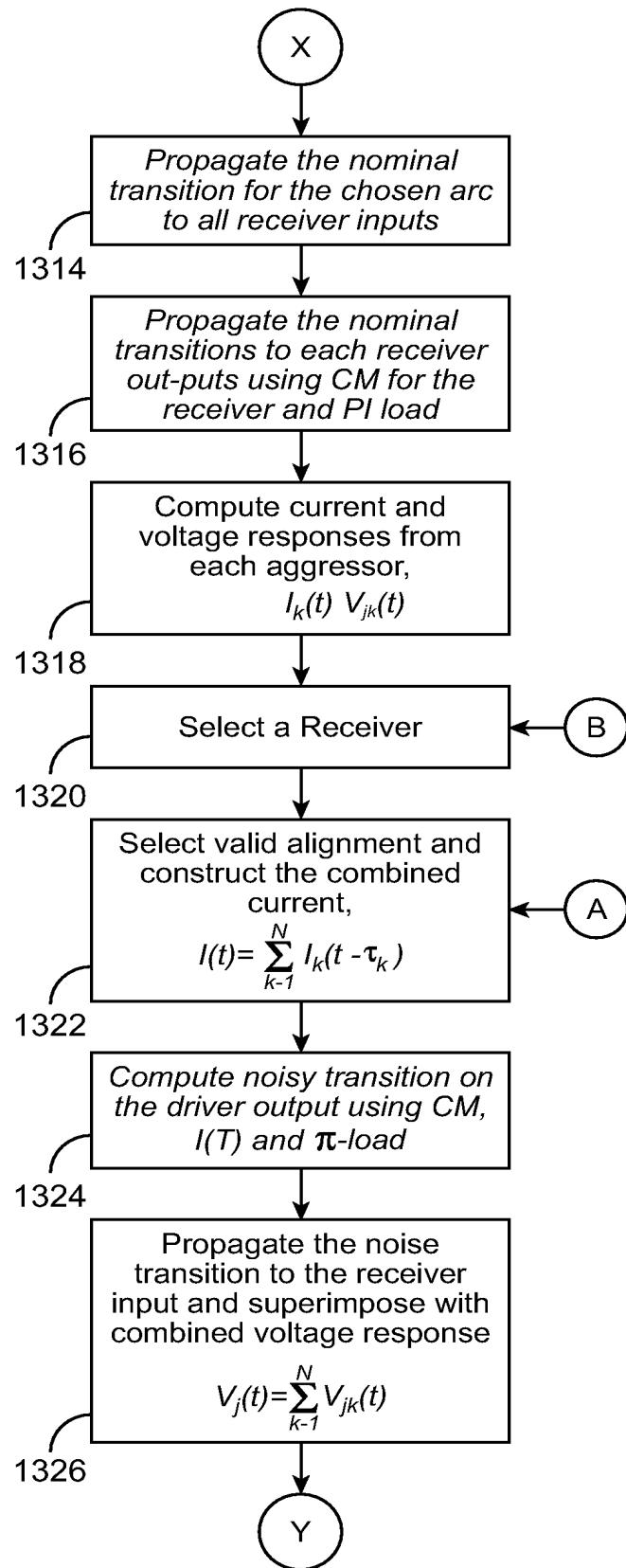


FIG. 13B

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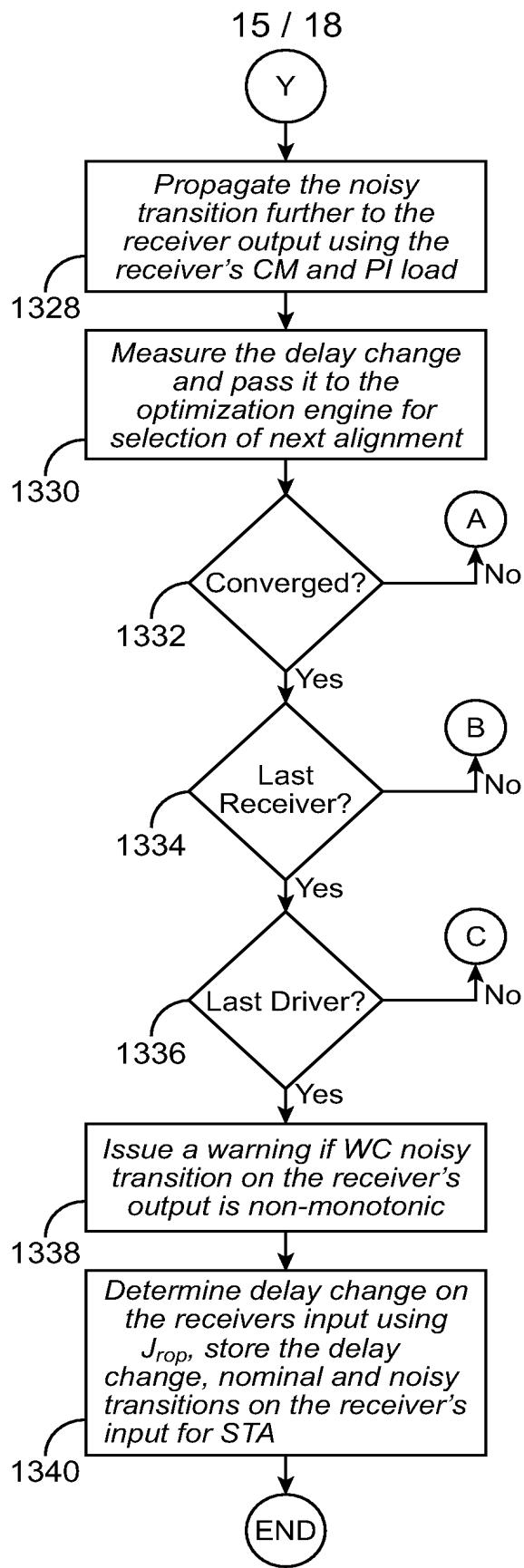


FIG. 13C

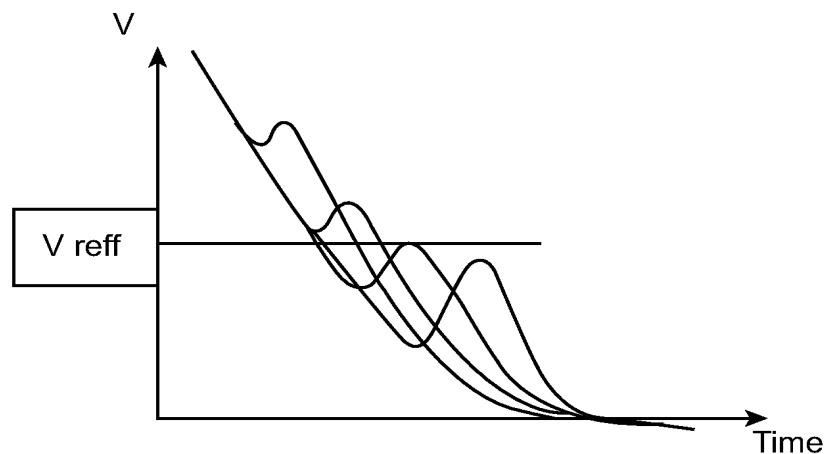


FIG. 14A

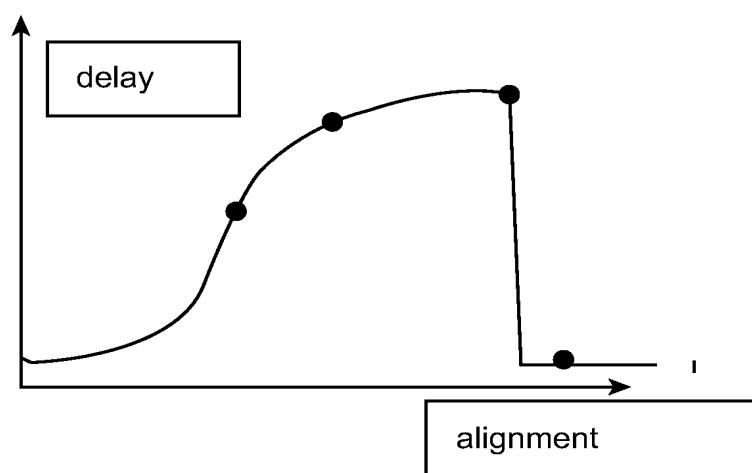


FIG. 14B

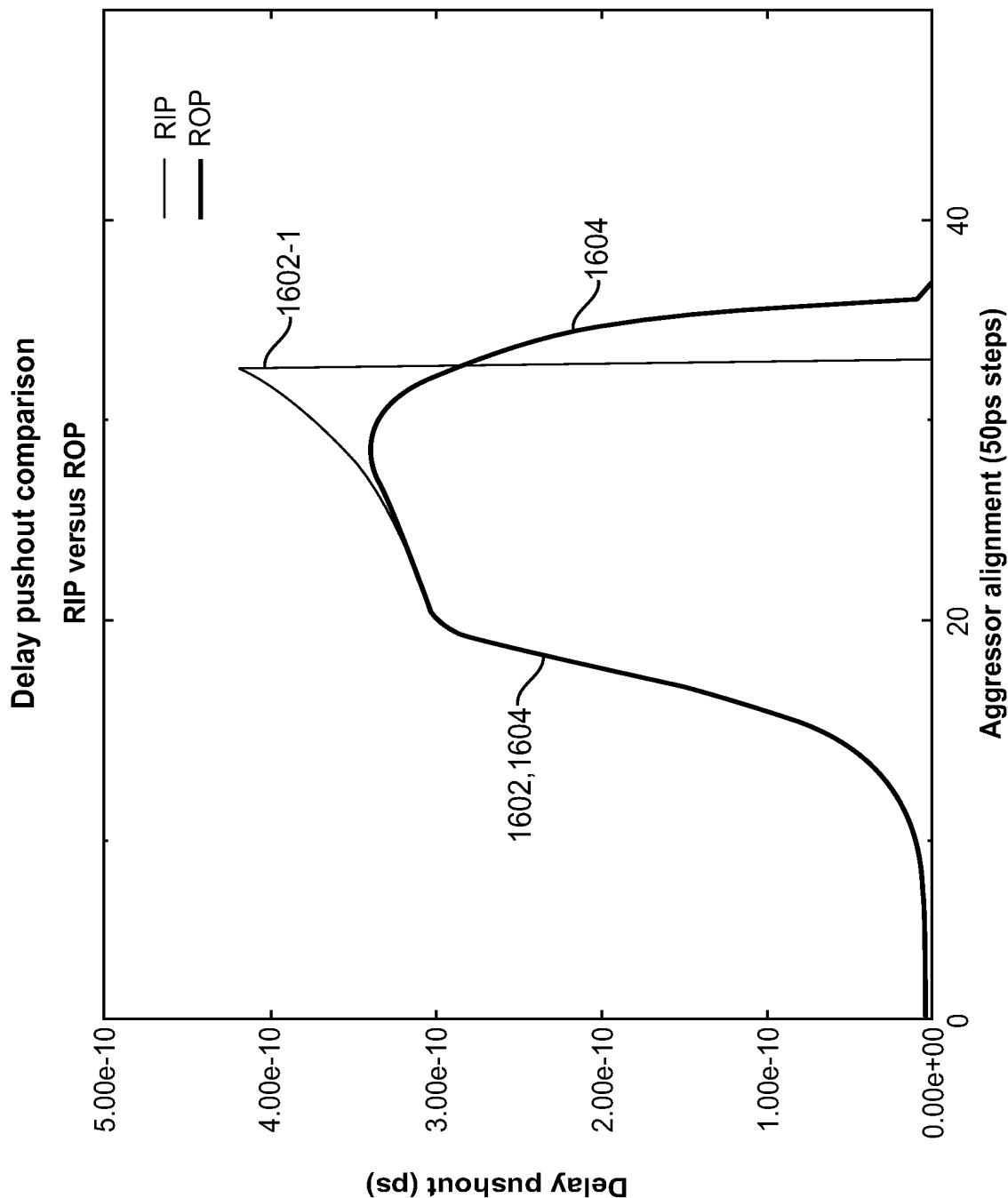


FIG. 15

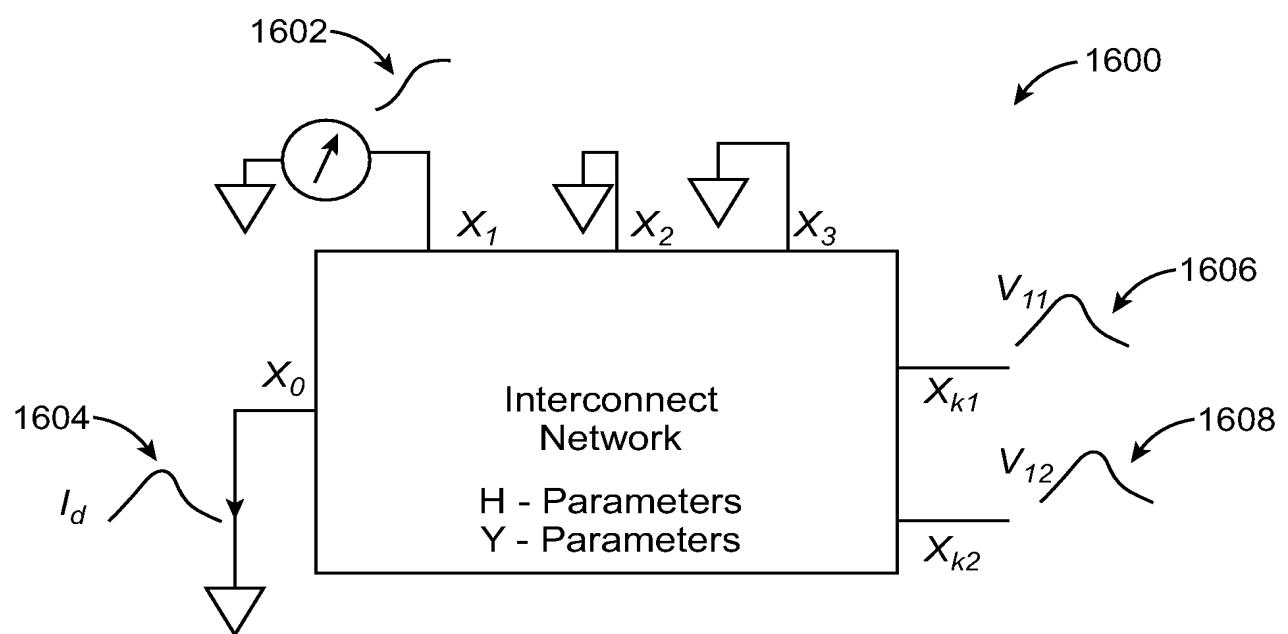


FIG. 16